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**CML00107D-P01**

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| FOREIGN PATENT DOCUMENTS |                          |   |                                |  |   |   |
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| Examiner<br>Initials*    | Cite<br>No. <sup>1</sup> | Foreign Patent Document   | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines,<br>Where Relevant Passages<br>or Relevant Figures Appear | † |
|                          |                          | Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (If known) |                                |  |   |   |
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PTO/SB/088 (10-01)  
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| Substitute for form 1449B/PTO   |   | <b>Complete if Known</b> |                 |                        |               |
|   |   | Application Number       | 10/657,793      |                        |               |
| <b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br>(use as many sheets as necessary) |   | Filing Date              | 9/8/2003        |                        |               |
|   |   | First Named Inventor     | Norris et al.   |                        |               |
|   |   | Art Unit                 | 2185            |                        |               |
|   |   | Examiner Name            | KIM, Hong Chong |                        |               |
| Sheet   | 1 | of                       | 1               | Attorney Docket Number | CML00107D-P01 |

| OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS |              |   |                |
|---|--------------|---|----------------|
| Examiner Initials*                                | Citation No. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
|   |              | Al-Mouhamed, M., "Lower Bound on the Number of Processors and Time for Scheduling Precedence Graphs with Communication Costs", IEEE, December 1990  |                |
|   |              | Samadzadeh, F. et al., "A Heuristic Multiprocessor Scheduling Algorithm for Creating Near-Optimal Schedules Using Task System Graphs", ACM, 1992  |                |
|   |              | Prasanna, G.N., et al., "Generalized Multiprocessor Scheduling for Directed Acyclic Graphs", IEEE, 1994   |                |
|   |              | Kwok, Y. et al., "Static Scheduling Algorithms for Allocating Directed Task Graphs to Multiprocessors", ACM, December 1999  |                |
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|--------------------|--------|-----------------|---------|
| Examiner Signature | H. Kim | Date Considered | 6/30/06 |
|--------------------|--------|-----------------|---------|

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